

CPLM
5/28/03

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

#15 / RESPONSE
D. EVANS
6.5.03

In re Appln Of: NAKAYAMA

Serial No.: 09/593,891

FAX RECEIVED

Filed: June 14, 2000

MAY 28 2003

For: SEMICONDUCTOR STORAGE DEVICE TECHNOLOGY CENTER 2800

Group: 2815

Examiner: CHRIS C. CHU

DOCKET: NEC DP-624

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450AMENDMENT D UNDER RULE 116

Dear Sir:

This Amendment is being filed in response to the Final Action mailed March 3, 2003.

REMARKS

The rejection of claims 1-8 under 35 USC § 103(a) as being unpatentable over Warren (U.S. Patent No. 5,905,639) in view of Bruce et al. (U.S. Patent No. 5,976,911) and further in view of Rostoker (U.S. Patent No. 5,767,570) is in error. Claim 1 requires that "the bonding pads on said upper chip that connect to the bonding pads of said substrate are disposed on the lower surface of said upper chip." None of these references teaches that the bonding pads on the upper chip are located on the lower surface of that chip. Thus, no combination of the art can achieve or render obvious claim 1 or any of the claims 2-8 dependent thereon.

In the Action, the Examiner admits that Warren and Bruce et al. do not teach that the bonding pads on the upper chip are located on the lower surface of the chip, but claims that the newly cited Patent to Rostoker, FIG. 1, teaches this feature (Detail Action, p. 3). More

HAYES SOLOWAY P.C.
130 W. CUSHING ST.
TUCSON, AZ 85701
TEL. 520.882.7623
FAX. 520.882.7643175 CANAL STREET
MANCHESTER, NH 03101
TEL. 603.668.1400
FAX. 603.668.8567

FAX RECEIVED**HAYES SOLOWAY, P.C.**

MAY 28 2003

130 W. Cushing Street
Tucson, Arizona 85701
Telephone (520) 882-7623
Facsimile (520) 882-7643

TECHNOLOGY CENTER 2800

Client-Matter Number
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TO: Examiner
Chris C. Chu

UNITED STATES
PATENT &
TRADEMARK
OFFICE

FACSIMILE NO.:
(703) 308-7722

FROM: Norman P. Soloway, Esq.

MESSAGE:

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